**Shivam Chandrakant Nikam**

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**Mobile No**: +919011974225

**Objective:**

A skill test engineer who specializes in identifying, reporting and tracking software defects. A result oriented professional who ready to work a fast paced work environment and is able to deliver on time with quality and possess a strong ability to perform individually.

**Professional Summary**:

* Familiar with all phases of software development life cycle (SDLC) and software test life cycle (STLC).
* Understanding, analyzing and streamlining Test requirements and Defect Management.
* Knowledge of different levels (functional, non-functional, White box, Regression and API) Testing.
* Knowledge in writing test cases, test plan, test scenario.
* Basic Knowledge of SQL, Agile Methodology.
* Good communication with excellent relationship building and interpersonal skills.
* Possesses a flexible and detailed oriented attitude with strong analytical, problem solving and organizational building abilities.

**Technical Certification:**

* Diploma in Software Testing from Pune.

**Academic Details:**

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| --- | --- | --- | --- |
| **Qualification** | **University/Board** | **Year of passing** | **Percentage (%)** |
| B.E. in E&TC | Shivaji | 2016 | 61.68 |
| Diploma in E&TC | MSBTE | 2013 | 60.00 |
| S.S.C | Nashik | 2008 | 73.38 |

**Software skills:**

**Programming language**: C.

**Office Proficiency**: MS Office (Excel, word and PowerPoint), operating system windows.

**Testing Tools**: Mantis and basic of selenium.

**Internship Project:**

|  |  |
| --- | --- |
| Project Title: | Testing of In-flow Inventory management software.  (project done at mind scripts at Viman Nagar, pune) |
| Project Description: | In-flow inventory is a management system designed for Item-based business, In-flow software aim is to save users time on paper work, fulfill customer needs in excess inventory and solution used by variety of businesses including retailer, e-commerce companies, government agencies and health firms. |
| Roles and Responsibilities: | Analyzing and streamlining requirements.  Writing test cases for different kind of In-flow modules.  Executing test cases and reporting defects. |

**Final Academic Project:**

|  |  |
| --- | --- |
| Project Title: | Designed of 8 bit LCD processor on FPGA using Verilog.  (project done at Dnyanshree Institute of Engineering and Technology) |
| Project Description: | 8 bit LCD processor works as a hardware accelerator, LCD interface and user instruction have been designed to manifest variety of message display and Entire logic and result are verified thorough simulation. |

**Extra-curricular Activities:**

* Participated in Texas Instrument India analog maker competition.
* Paper presented on ‘phased locked loop’.
* Participated in Kabbadi and Chess competition and head of culture activity at college level

**Personal Details:**

**Full Name:** Shivam Chandrakant Nikam.

**Date of birth:** 23rd June 1992

**Languages known:** English, Hindi and Marathi (Native).

**Gender:** Male.

**Declaration:**

I hereby declare that the above information is true to the best of my knowledge and belief.

**Date**:

**Place**: - **Shivam Chandrakant Nikam**